

Abstract of the Disclosure

A first input transistor of a current mirror, in which one end is connected to a first constant current source and another end is connected to a reference potential (for example, the ground), serves as a current mirror input. A second input transistor, in which one end is connected to a second constant current source, is disposed with being separated from the first input transistor by a predetermined distance. A plurality of output transistors is distributed between the first and second input transistors. The gate-source voltages of the output transistors are substantially equal to those of the first and second input transistors. Therefore, it is possible to provide to a current mirror circuit which has a large number of output transistors, an influence due to the wiring resistance of a feeder line are remarkably reduced without increasing the wiring area for forming the feeder line.